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CONFIRMATION NO. 7230

SERIAL NUMBER 10/773,853	FILING DATE 02/09/2004  RULE	CLASS 257	GROUP ART UNIT 2826	ATTORNEY DOCKET NO. ONS00470	
APPLICANTS  Gordon M. Grivna, Mesa, AZ;  <i>gr</i> ** CONTINUING DATA *****  <i>gr</i> ** FOREIGN APPLICATIONS *****  IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/05/2004					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged		STATE OR COUNTRY AZ	SHEETS DRAWING 5	TOTAL CLAIMS 20	INDEPENDENT CLAIMS 3
ADDRESS Mr. Jerry Chruma Semiconductor Components Industries, L.L.C. Patent Administration Dept - MD/A700 P.O. Box 62890 Phoenix , AZ 85082-2890					
TITLE Semiconductor device having reduced capacitance to substrate and method					
FILING FEE  RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____		

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